

Docket No. 205263US2



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Yasuhiko KURIYAMA

GAU: 2811

SERIAL NO: 09/817,216

EXAMINER: CHOE, HENRY

FILED: March 27, 2001

FOR: SEMICONDUCTOR POWER AMPLIFIER AND MULTISTAGE MONOLITHIC INTEGRATED CIRCUIT

REQUEST FOR EXTENSION OF TIME
UNDER 37 C.F.R. 1.136

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

It is hereby requested that a two month extension of time be granted to JUNE 28, 2002 for

filing a response to the Official Action dated: JANUARY 28, 2002

responding to the requirements in the Notice of Allowability dated:

filing the Formal Drawings. The Issue Fee due has been timely filed.

responding to the Notice to File Missing Parts of Application dated:

filing a Notice of Appeal. A timely response to the final rejection, due has been filed.

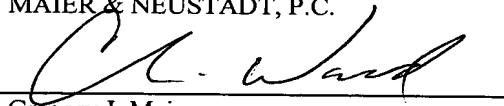
filing an Appeal Brief. A Notice of Appeal was filed on:

Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee amount shown below is reduced by one-half.

The required fee of \$400.00 is enclosed herewith by check and any further charges may be made against the Attorney of Record's Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.


Christopher D. Ward

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TECHNOLOGY CENTER 2800
JUL -8 2002